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	Filing Date	June 24, 2003
	First Named Inventor	Eric E. Edwards
	Examiner Name	Terry Lee Englund
	Art Unit	2816
	Patent No.	
	Attorney Docket Number	X-1330 US

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (with Recordation Cover Sheet)	<input type="checkbox"/> After Allowance Communication to
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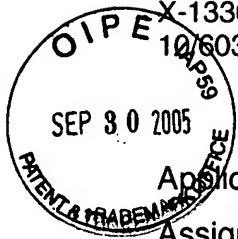
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X-1330 US
10/603,217

PATENT
CONF. NO.: 4304

IN THE UNITED STATES PATENT OFFICE

Applicant: Eric E. Edwards

Assignee: Xilinx, Inc.

Title: "Voltage Detector"

Ser. No.: 10/603,217

File Date: 06/24/2003

Examiner: Terry Lee Englund

Art Unit: 2816

Docket No.: X-1330 US

Conf. No.: 4304

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AMENDMENT IN RESPONSE TO THE FINAL OFFICE ACTION

Dear Sir:

In response to the Final Office Action mailed from the Patent Office on July 28, 2005, please amend the application as follows:

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 6 of this paper.